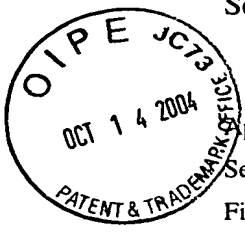


Serial No.: 10/692,079

PATENT
PU030092

162



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Robert Allen Castlebary et al.
Serial No. : 10/692,079
Filed : October 23, 2003
For : TECHNIQUE FOR OVERSAMPLING TO REDUCE JITTER
Examiner : Not Yet Known
Art Unit : 2631

INFORMATION DISCLOSURE STATEMENT

- ☒ 1 Pursuant to 37 CFR 1.97(b)
[within 3 months of filing or prior to 1st Office Action]
☐ 2 Certification Pursuant to 37 CFR 1.97(c)
[before Final Office Action or Allowance]
☐ 3 Fee Payment Pursuant to 37 CFR 1.97(c)
[before Final Office Action or Allowance]
☐ 4 Petition, Certification & Petition Fee Payment Pursuant to 37 CFR 1.97(d)
[before issue fee payment]

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following are submitted in the above-identified application in compliance with 37 CFR 1.97 & 1.98:

☒ 5 A list of documents on form PTO-1449 together with copies of each identified document and a translation or a concise explanation of each non-English language document is enclosed herewith.

This paper is submitted in accordance with:

- ☒ 6 37 CFR 1.97(b): [within 3 months of filing or prior to 1st Office Action]
☐ 7 37 CFR 1.97(c): [before Final Office Action or Allowance, whichever is earlier]; and
☐ 8 The required certification made in item 11 below; or
☐ 9 The \$240.00 fee specified in 37 CFR §1.17(p) for submission of this Information Disclosure Statement is authorized in item 14 below.
☐ 10 37 CFR § 1.97(d): [before issue fee payment]; and
(a) This is a petition for consideration of the subject Information Disclosure Statement. The petition fee (\$130.00) required by 37 CFR 1.17(i)(1) is authorized in item 14 below. (Direct this letter to "Attention PETITIONS EXAMINER" and if applicable include batch locator information: e.g., "Allowed Files, Batch _____, Date of Allowance _____"), and
(b) The required Certification is stated in item 11 below.

☐ 11 Certification

☐ 12 Each item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; or

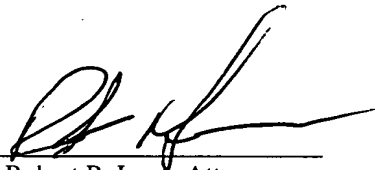
☐ 13 No item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing this document after making reasonable inquiry, was known to any individual designated in 37 CFR 1.56(c) more than three (3) months prior to the filing of this Statement.

☒ 14 Please charge the applicable fees associated with the submittal of this Information Disclosure Statement to Deposit Account No. 07-0832. An original and one (1) copy of this document is enclosed.

Respectfully Submitted,

Robert Allen Castlebary et al.

BY:


Robert B. Levy, Attorney
Registration No. 28,234
(609) 734-6820

THOMSON multimedia Licensing Inc.
Patent Operations
P.O. Box 5312
Princeton, New Jersey 08543-5312

DATE:

10/12/04

Certificate of Mailing under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in a postage paid envelope addressed to: Mail Stop Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

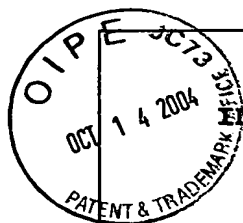
Date:

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Signature

Lori M. Kluwin

Form IDS Ltr.
(09/01)



U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.
PU030092

SERIAL NO.
10/692,079

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97**

(Use several sheets if necessary)

APPLICANTS
Robert Allen Castlebary et al.

FILING DATE
October 23, 2003

GROUP
2631

U.S. PATENT DOCUMENTS

EXAMINE INITIAL	DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
AA	5,761,254	6/2/1998	Behrin	375	355	

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION Yes No

OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)

AB	Park J-Y et al.: "A 1.0 GBPS CMOS Oversampling Data Recovery Circuit with Fine Delay Generation Method" IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, Vol. E83-A, No. 6 13 July 1999 (1999-07-13), Pages 1100-1105, XP001032162

EXAMINER

DATE CONSIDERED

SUBMITTED BY:

Robert B. Levy

REG. NO.: 28,234

DATE: 10/12/04